Rapidio The Embedded System Interconnect

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30 years of IC packagingHow Open Standards Architectures are Meeting MOSA Requirements (#1/3 in a series) What is an embedded system \u0026 how is it central to IoT product development? What Is an Embedded System? *Panel III I Seminario Internacional de e Justicia* WiFröst: Bridging the Information Gap for Debugging of Networked Embedded Systems Upload \u0026 Annotate Free Ebooks in Kurzweil 3000 **RISC Architecture : AVR, POWER, PowerPC,Power** *Rapidio The Embedded System Interconnect*
Mercury is co-developer of the RapidIO interconnect architecture ... is the leading provider of high-performance embedded, real-time digital signal and image processing computer systems. Mercury's ...

Mercury Computer Systems, Inc. Announces Availability of its Serial RapidIO-Based Silicon IP Core
The 25xN RapidIO Specification 4.1 offers 100 gigabits per second (Gbps) per x4 port to provide a proven, open standard interconnect fabric for systems requiring heterogeneous ... and custom and ...

Mobiveil's 25xN RapidIO 4.1 compliant controller IP achives production status
We made a joke – sort of – many years ago when we started this publication that the future compute engines would look more like a GPU card than they did a ...

How The FPGA Can Take On CPU And NPU Engines And Win
Mouser Electronics, Inc. now offers COM-HPC interconnect solutions from Samtec. Designed to meet the COM-HPC standard recently introduced by the PCI Industrial Computer Manufacturers Group (PICMG), ...

Now at Mouser: Samtec's COM-HPC interconnect solutions for AI, Industrial, IoT applications
Applied Materials rolled out a semiconductor fab tool this month that gives its a new way to engineer copper interconnects in advanced logic chips, solving a stumbling block to future 3-nanometer ...

Applied Materials Says New Tool Eases Resistance in Chip Interconnects
The company's COM-HPC interconnect solutions are based on Samtec's AcceleRate HP high-performance arrays, providing design engineers with both greater scalability and enhanced performance for next-gen ...

Mouser stocking Samtec's COM-HPC interconnect solutions
These letters examine what the world could look like then — and how we can make the best scenario happen. If you still have such a thing as a doctor's office, it might look something like the one I ...

What universal healthcare should look like
Substantial government and private investments in the semiconductor space helped supply levels to improve slightly in May. And because this improvement trend is likely to continue, we think ...

2 Semiconductor Stocks to Buy in July, 2 to Avoid
Arm enhanced its AMBA 5 coherent interconnect to support MTE. Tag check logic is typically built into the system-level cache, with tag checking and tag caching occurring ahead of the DRAM interface.

Sponsored Post: Keep Your App's Memory Safe with Arm Memory Tagging Extension (MTE)
Market research reports show that 10% or more of all FPGAs are used by military and aerospace systems. Clearly FPGA provides major advantages in the flexibility to adapt to changing standards and ...

The Advantages of Embedded FPGA for Aerospace and Defense
That technology, called embedded wafer ... "With the increase in system complexity, and the heterogeneous integration of large, high I/O dies into a package, the number of redistribution traces that ...

Fan-Out Packaging Options Grow
Next-generation edge processing technology enables advanced AI applications, autonomous platforms and smarter missions HDS6705 Blade Server Mercury's HDS6705 blade servers are powered by artificial ...

New OpenVPX blade server provides advanced security and data center-class processing
"Interconnect technology is increasingly vital ... Synopsys' broad DesignWare IP portfolio includes logic libraries, embedded memories, IOs, PVT monitors, embedded test, analog IP, interface ...

Synopsys Expands Multi-Die Solution Leadership with Industry's Lowest Latency Die-to-Die Controller IP
Rambus is engaging with the full ecosystem including cloud, system and memory companies, to accelerate the development and enablement of CXL memory interconnect solutions. For more information ...

Rambus Advances New Era of Data Center Architecture with CXL™ Memory Interconnect Initiative
A bulk purchasing program that makes rooftop solar more affordable faces a hurdle because of solar's popularity.

Solar bulk buy program surges in Savannah but may hit a roadblock with net metering
Concurrently, CDSG announced that Trenton Systems Inc. in Lawrenceville, Ga., will offer Citadel SSDs in the company's next and future generations of rugged servers, workstations, embedded systems ...

Self-encrypting SSD data storage for trusted computing in military and intelligence introduced by DIGISTOR
Aldec extended its TySOM family of embedded prototyping boards with the introduction ... Corigine debuted its MimicPro prototyping systems based on Xilinx UltraScale FPGAs. The prototying system ...

Week In Review: Design, Low Power
FastNICs seeks to find ways of improving network stack performance in military embedded computing systems to realize ... servers and the network links that interconnect them, DARPA officials ...

Raytheon joins DARPA project to speed application software by improving network interface cards (NICs)
"This initiative is highly complementary to our existing server DIMM chipset business and brings together our unique combination of semiconductor and system expertise to develop breakthrough ...