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--(BUSINESS WIRE)--Aldec, Inc., a pioneer in mixed HDL language simulation and hardware-assisted ... (AWS), HES-DVM Proto CE can be used for FPGA-based prototyping of SoC / ASIC designs and ...

Aldec Launches HES-DVM Proto 'Cloud Edition' - Giving Engineers Easier Access to FPGA-based ASIC & SoC Prototyping
Markus Pfaff, Fachhochschule Hagenberg Hagenberg, Austria Abstract: This paper shows a way to connect a FPGA ... (e.g. a CPU, a card with a PCI interface, ...). The purpose of the system is that the ...

Connecting reality and simulation: Couple high speed FPGAs with your HDL simulation
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Corigine Delivers a Next-Generation Prototyping System for ASIC and Pre-Silicon Software Development
A description language of this filter is used for simulation and ... Fig 3. FPGA: Basic Structure The features of a logic block (called a Configurable Logic Block (CLB) by Xilinx) shown in figure 4 is ...

Systolic FIR Filter Based FPGA
Lattice Semiconductor Corporation (NASDAQ: LSCC), the low power programmable leader, today announced the Lattice Education Competence Center (LEC2) will host a series of free online training courses ...

Lattice Announces LEC2 TechWeb Trainings to Help Developers Create Applications Based on Low Power Lattice FPGAs
Control Engineering - CIMData announced the release of the CIMData Simulation and Analysis (S&A) Market Analysis Report. This report deepens the S&A market analysis ...

Simulation and analysis market grows in 2020
Xilinx isn't alone in attacking this space, but the combination of AI/ML acceleration, FPGA configurability, a high-speed fabric, and an Arm-based compute ... platforms, e.g., GPU-base solutions.

Edging AI with an AI FPGA
In specific terms, it is based on an experiment with a circuit controlled by an FPGA, a programmable electronic ... from being similar to working with a simulation, which we achieved with the ...

A remote laboratory for performing experiments with real electronic and communications equipment
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11 Myths About SoC/ASIC/FPGA Resets
This GAN works out about two thousand times faster than is possible with conventional simulation ... (FPGA), which are now used heavily in AI processing because of their flexibility, have also turned ...

Learning to design better
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