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Q. 4.13: The adder-subtractor circuit of Fig. 4.20(b) has the following values for mode input M and **Standard Books for ISRO, IES \u0026 GATE Preparation for ECE Digital Design 5th Edition Mano** Digital Design, fifth edition is a modern update of the classic authoritative text on digital design. This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications.

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Errata: Suspected Typos in M. Morris Mano & Michael D. Ciletti's Digital Design: with an Introduction to the Verilog HDL (5th edition, 1st printing) Most of the typos listed here have been fixed in the 6th edition.

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